| L Number | Hits | Search Text | DB | Time stamp |
|---|---|---|---|---|
| 35 | 10 | alignment adj between adj (ic or (integrated adj circuit)) adj chips | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:33 |
| 36 | 0 | alignment adj between adj (ic or (integrated adj circuit)) adj chip adj substrates | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:08 |
| 37 | 176 | alignment adj between adj substrates | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:45 |
| 38 | 37 | (alignment adj between adj substrates) and tolerance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:14 |
| 39 | 27 | (alignment adj between adj chips) and tolerance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:22 |
| 40 | 0 | (alignment adj between adj transistors) and tolerance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:23 |
| 41 | 20 | (alignment adj between adj die) and tolerance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:26 |
| 42 | 0 | (alignment adj between adj dice) and tolerance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:26 |
| 43 | 25 | (alignment adj between adj devices) and tolerance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:29 |
| 44 | 0 | (alignment adj between adj semiconductor adj devices) and tolerance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:29 |
| 45 | 17 | (alignment adj between adj semiconductor) and tolerance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:31 |
| 46 | 43 | (alignment adj between adj wafer) and tolerance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:31 |
| 47 | 38 | alignment adj between adj (ic or (integrated adj circuit)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:37 |
| 48 | 0 | first adj alignment adj tolerance adj range | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:37 |

| 49 | 19 | alignment adj tolerance adj range | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:53 |
|---|---|---|---|---|
| 50 | 7 | first adj alignment adj tolerance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:42 |
| 51 | 7 | second adj alignment adj tolerance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:42 |
| 52 | 124 | alignment adj between adj bases | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:45 |
| 53 | 0 | alignment adj tolerance adj range adj smaller | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/11/22 16:53 |